

Plasmonic Metal Nanopatterning and Its Applications in Optoelectronics

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Korea Institute of Machinery & Materials



Outlines

1 Laboratory Introduction

- Members / Facility
- Researches for Nanoimprint

2 Technology Overview

- **Basic principles**
- **Research scopes and applications**
- **Research directions**

3 Research Activities in Plasmonic Metal Nanopattern Process

- **Lift-off, Direct etch, 3D profile of metal nanostructures**
- **Embedded profile Ag nanopatterns**

4 Collaborations–Closing remarks

Members



➤ Nanoimprint

- Process, Tools, Functional mater.
- Appl.: R-RAM, Sensors, (O)LED

➤ 3D Multiscale Architecturing

- Nanowire structuring
- Nanomaterials self-assembly

➤ Plasmonic sensors, Light emitting

- Metal nanostructuring
- Metal NP. Self-growth

Staffs

- Lee, Eung-sug: Ultrafind fabrication, Nanomechatronics
- Jeong, Jun-ho: Nanolithography, Nanoimprint
- Choi, Jun-hyuk: Metal nanopatterning, Direct/Roll Imprint
- Choi, Dae-geun: Nanoimprint mater. Process chemistry
- Lee, Ji-hye: Nanowire, mask fabrication, biosensors
- Jeong, Joo-yeon: Electronics, Plasmonic optics



Facilities/Infra.

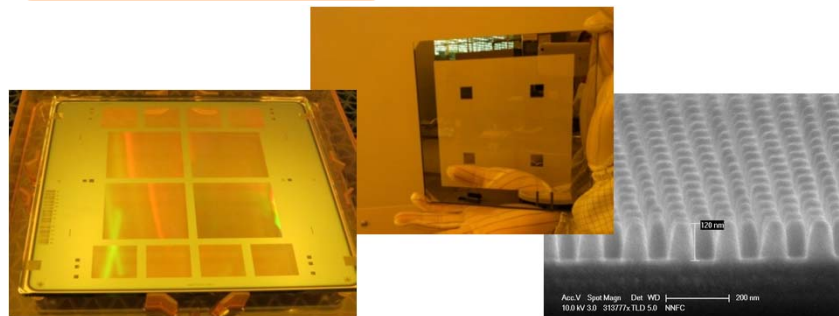
For Nanoimprint at wafer scale



For Nanoimprint in Large area

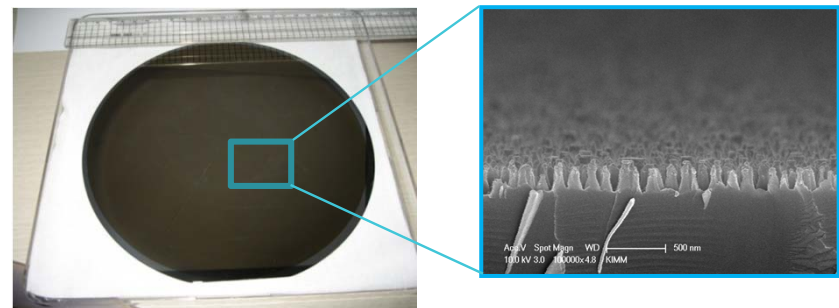


Master/Molds

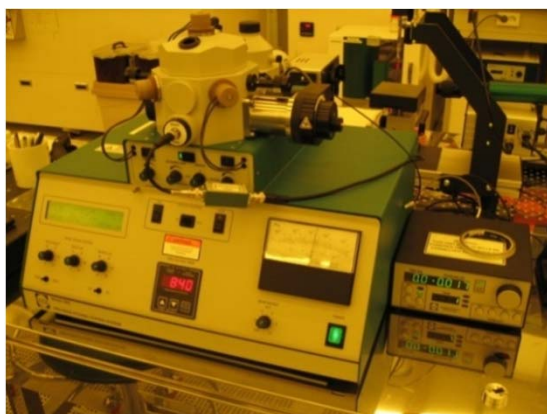


Large area Mold fabrication

Etched master at 8" wafer



Metal patterning infrastructures



증착속도에 따른 박막 Quality 비교

5.0 Å/S

v.s.

0.5 Å/S

Ag deposited
By E-beam

Silver Milling Test
w.r.t. the variables of current, time, and tilting angle

0°

60°

- Tilting angle ~0
- Current 300 μA
- Time 1min

Target Sub.

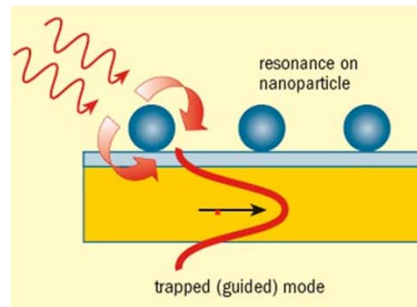
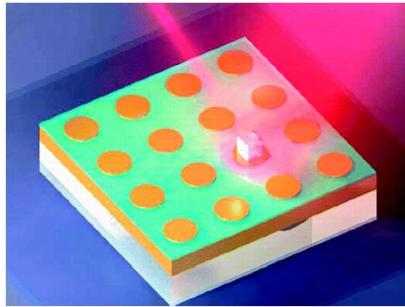
A

B



Technology Overview

Basic Principles



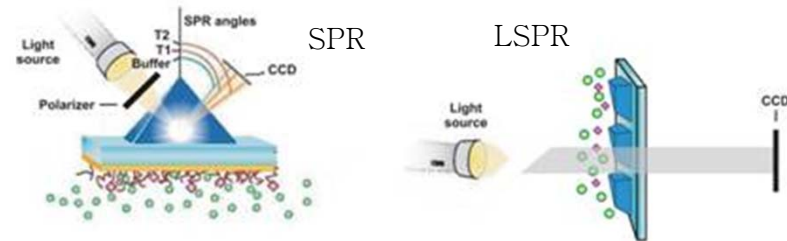
When illuminated outside



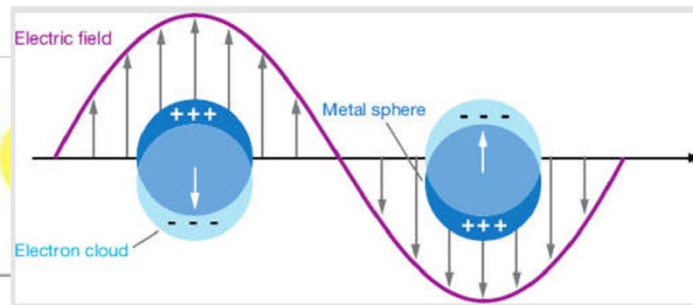
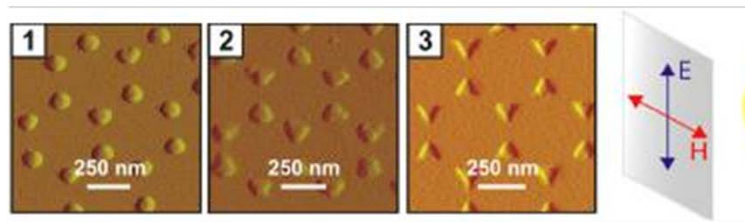
When illuminated inside

Surface plasmon resonance

- Photon–electron coupled phenomenon at resonant condition when illuminated on metal surface
- Collective oscillation of electrons propagating on the surface with positive dielectric constant
- Evanescent wave out of plane
- Locally amplified field formation



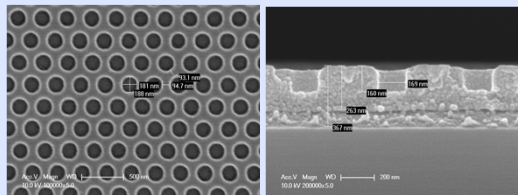
Localized surface plasmon resonance





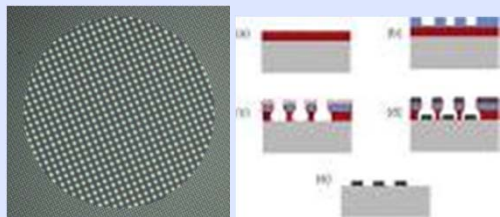
Research area

<Nanopatterning, Lithography>

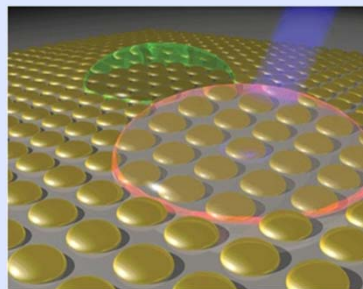
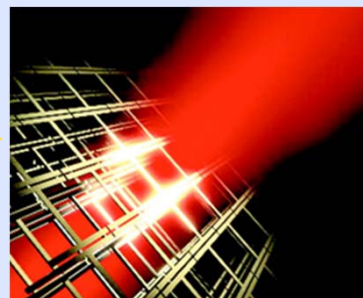
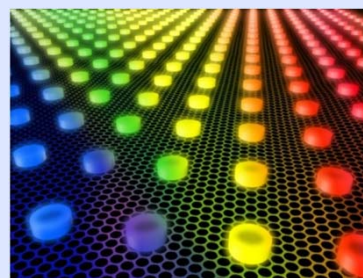


<Etching>

<Metal pattern, Lift-off>



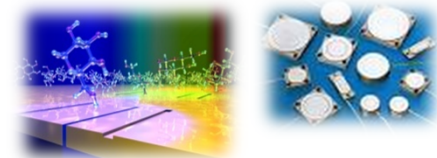
<Metal nanopattern array>



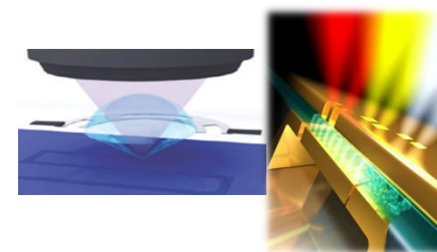
<Optoelectronic Applications>



<Optics-based Sensors>



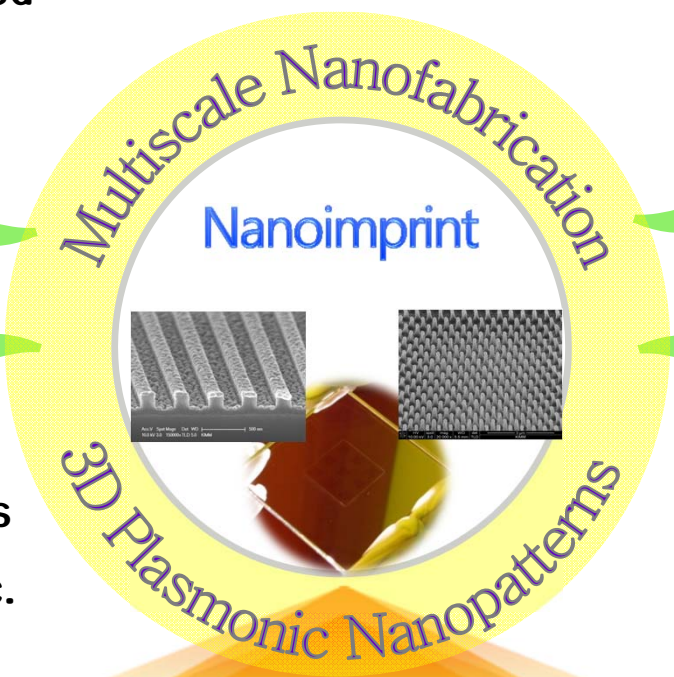
<Plasmonic Lithography>



Research directions

- Multilayer, Enlarged
- Solar cells

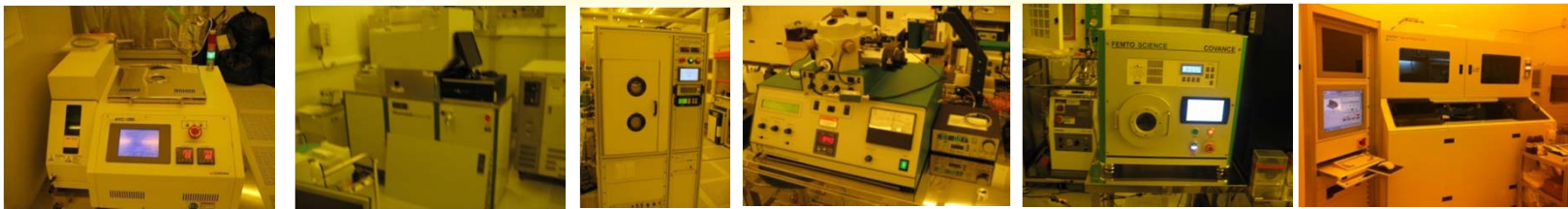
- Planarized Struc.
- Plasmonic Litho. Mask



- Plasmonic sensors
- Undercut 3D Struc.

- Optoelectronics
- Embedded structure profile

Infrastructure of metallic nanopatterning

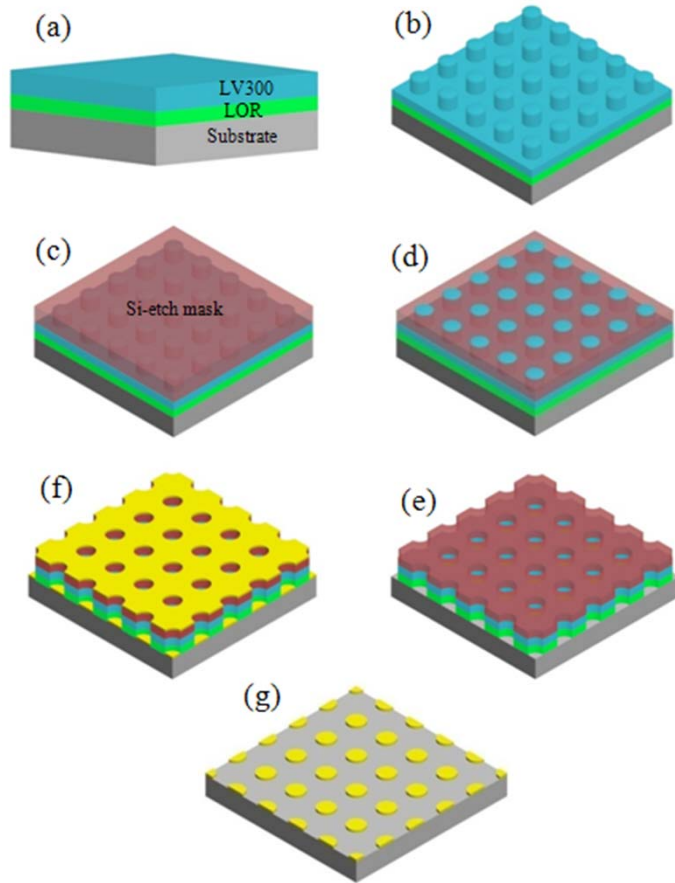




Research Activities

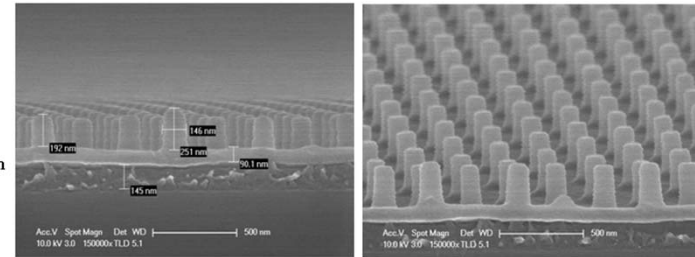
Lift-off for well-round shaped Metal dots

Process Scheme



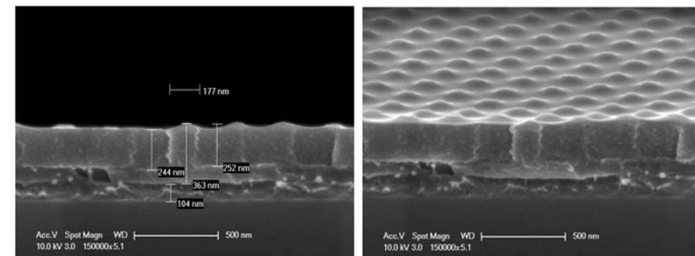
Step 2: Bilayer Imprint

- 150nm, Pillar형, 1:2 A.S.
- LOR 90nm / LV 300nm
- UV Imprint at 2 bar, 90s
- Transferred Height 250nm
- 잔류층 50~60nm



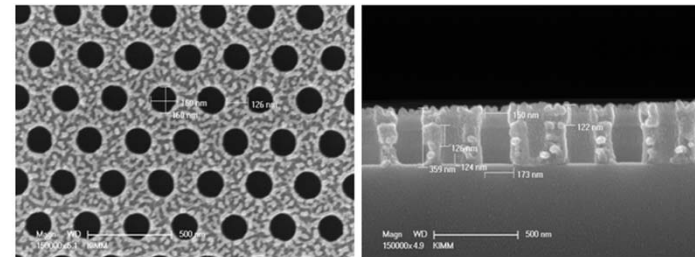
Step 3: Silpin 중착

- 4,000 rpm, 30sec
- 150도에서 3분 어닐링



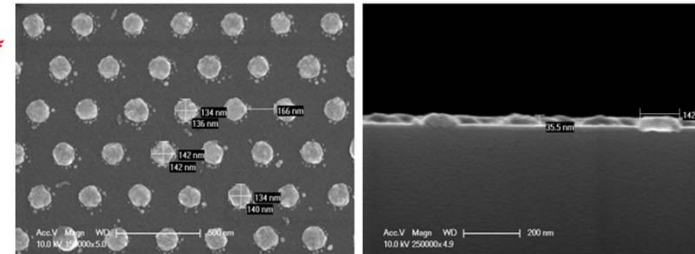
Step 5: Etch-down

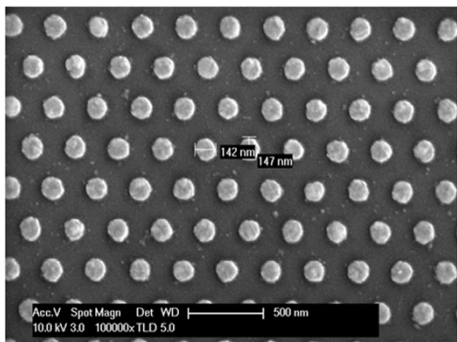
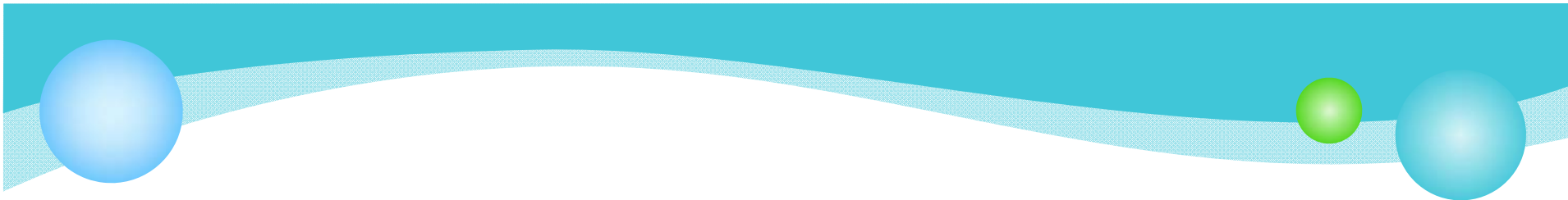
- Silspin Etch 20s, O₂+CHF₃
- Imprint Resist & LOR Etch : 50 sccm of O₂, 90s



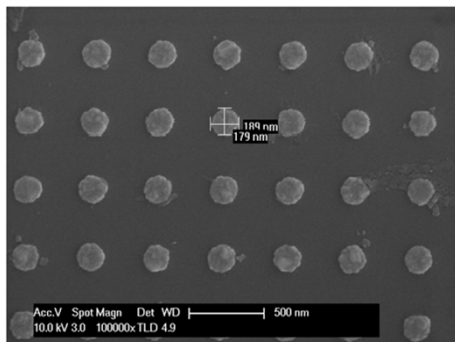
Step 7: Ag 중착 -> Lift-off

- Thermal Evap. 30nm
- Developer 400K

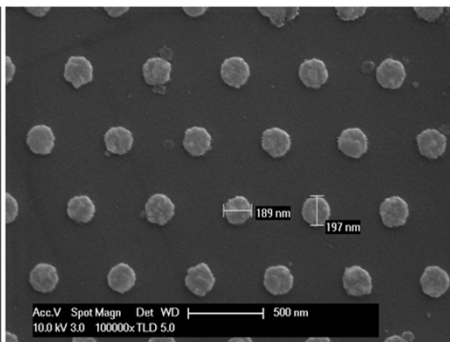




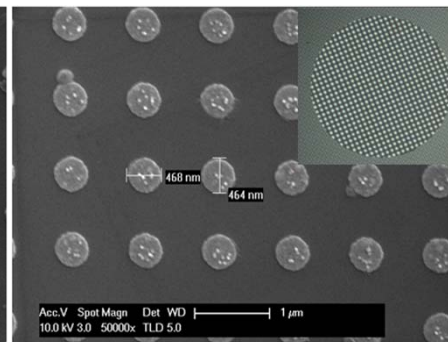
150nm Ag hexa
25nm thick



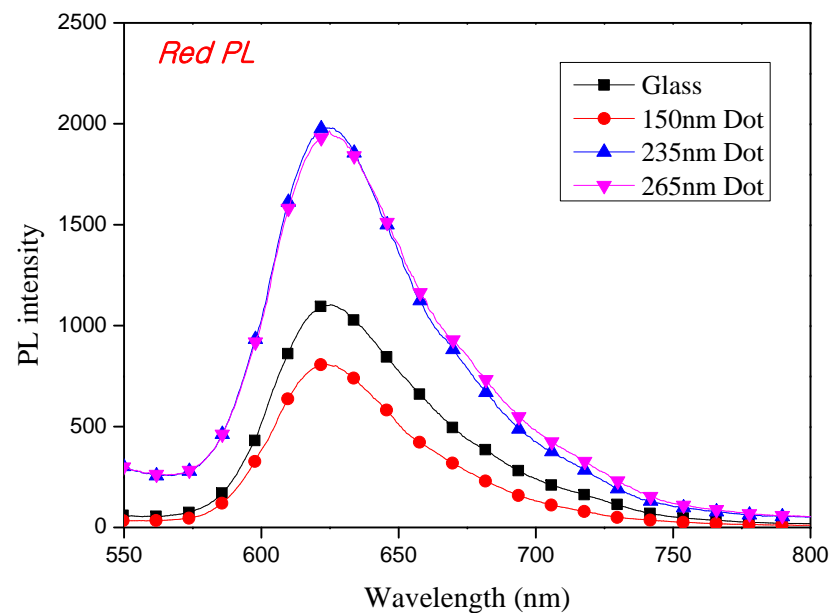
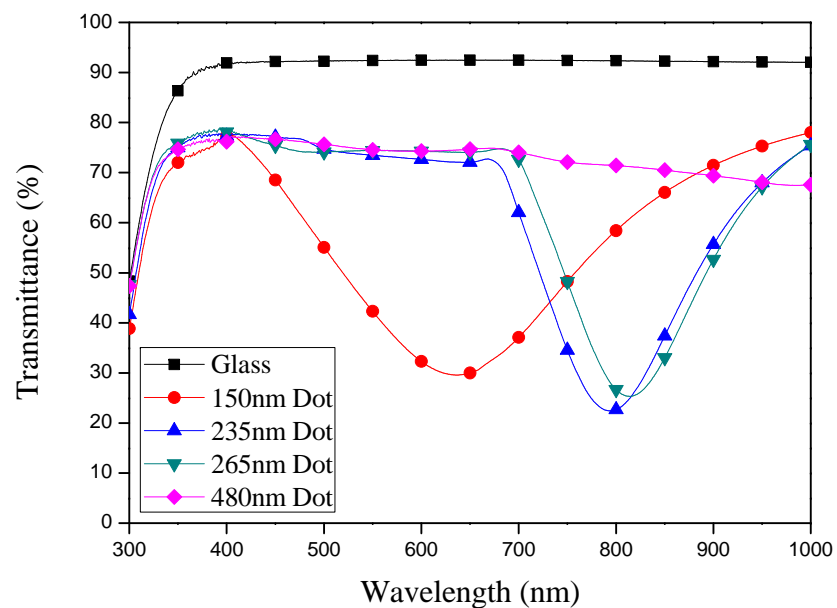
235nm Ag square
25nm thick



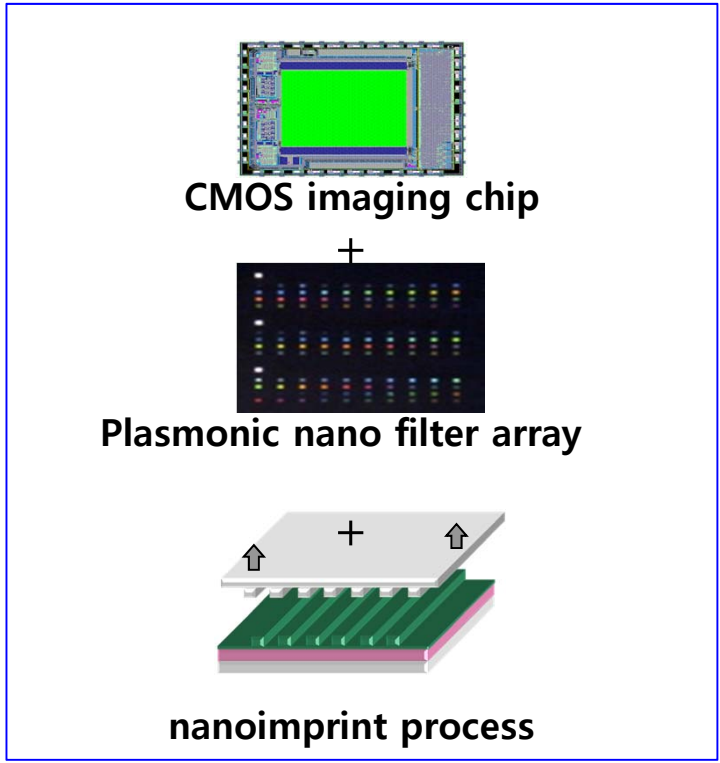
265nm Ag hexa
25nm thick



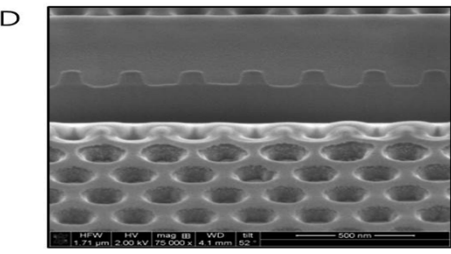
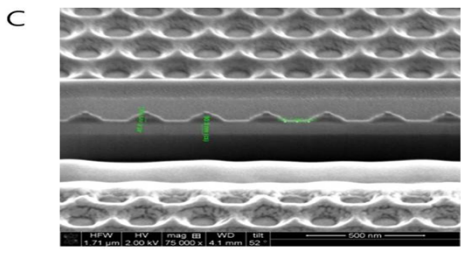
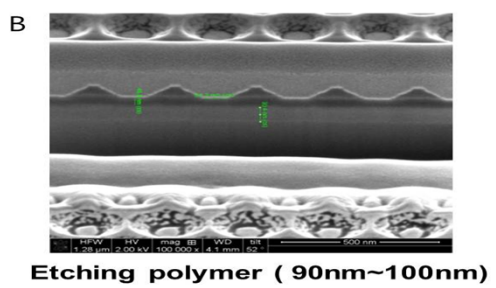
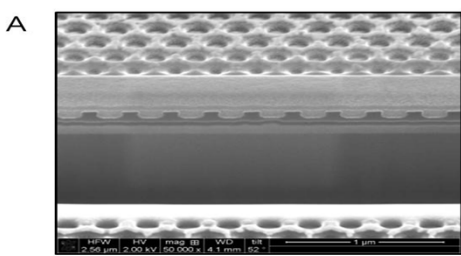
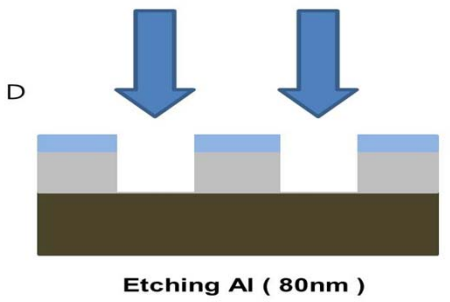
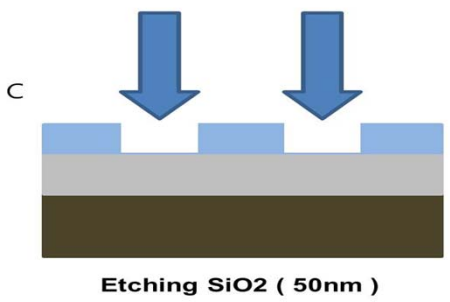
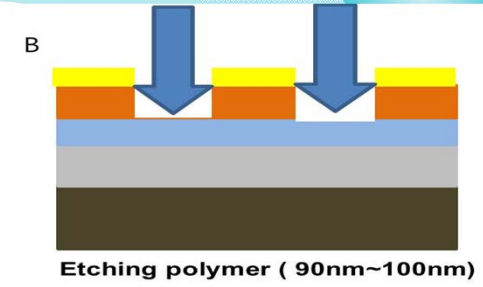
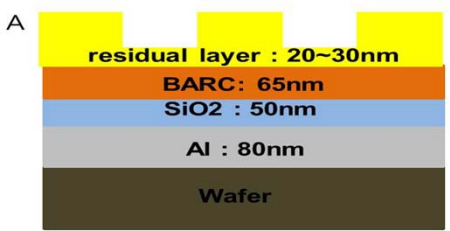
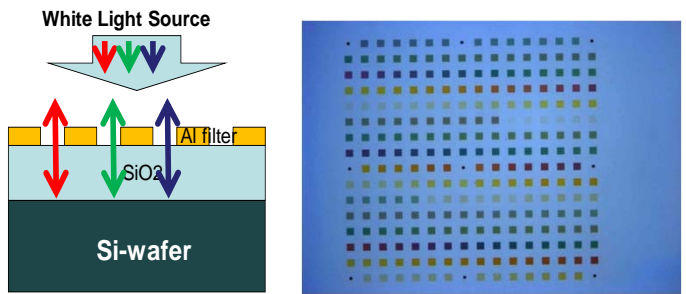
480nm Ag square
25nm thick



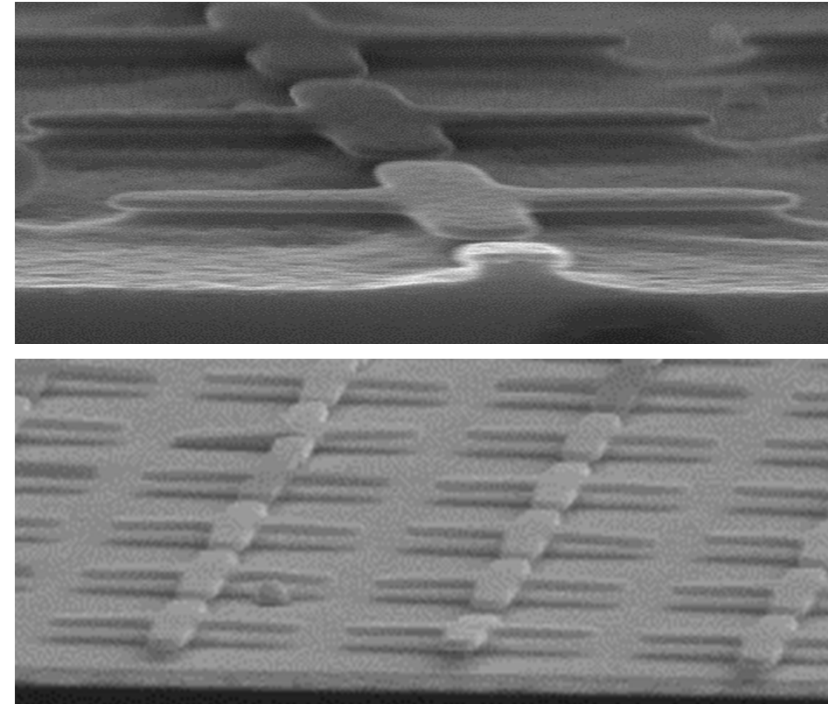
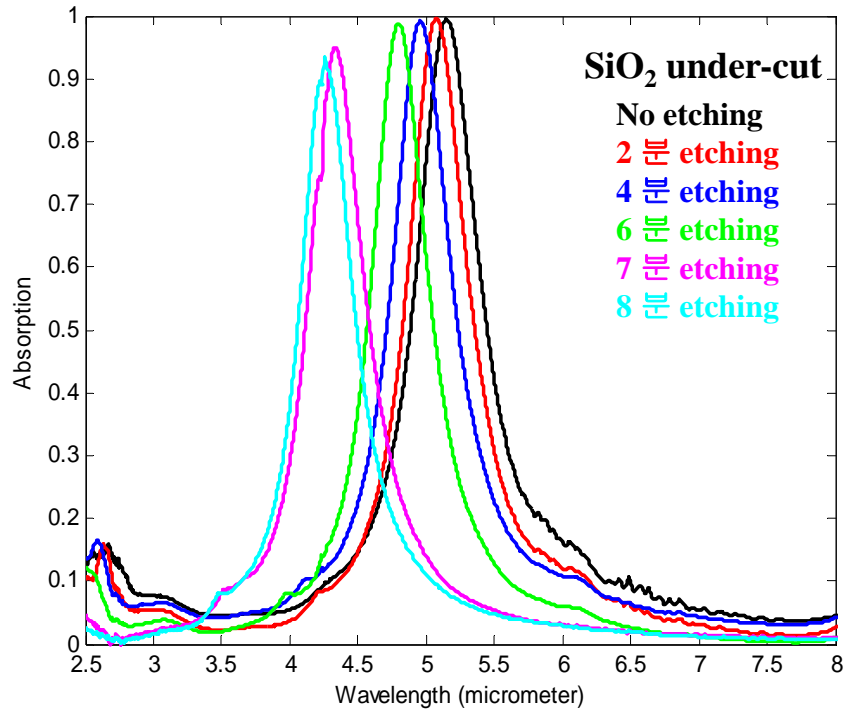
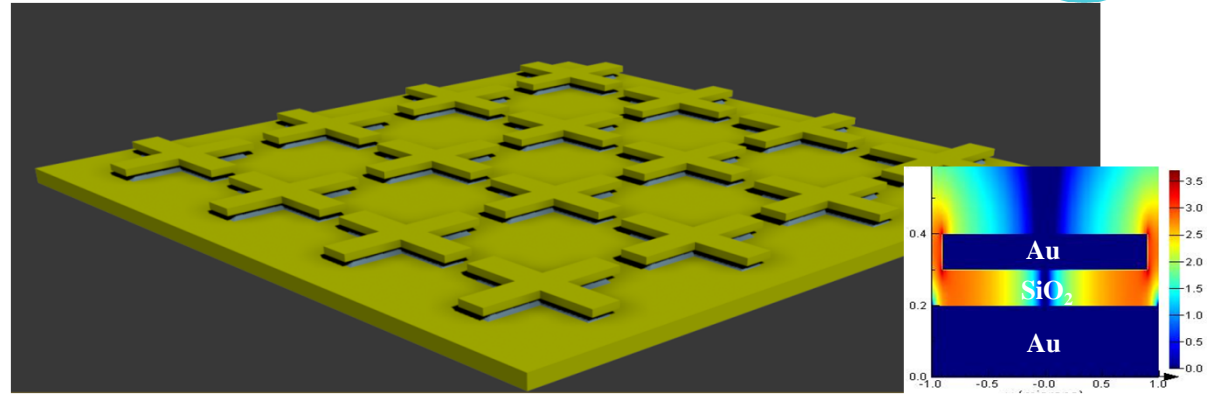
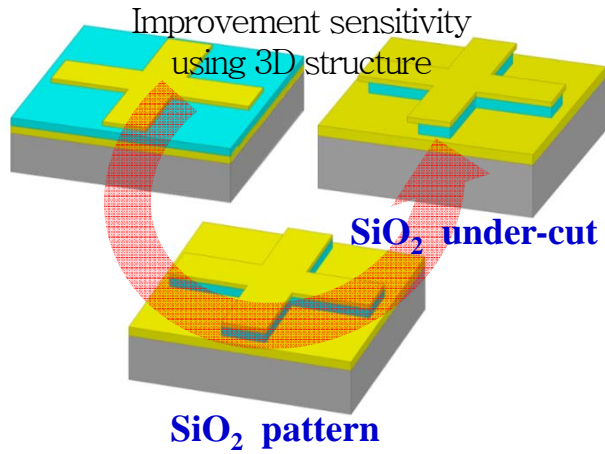
Metal direct etch, Nanoimprint



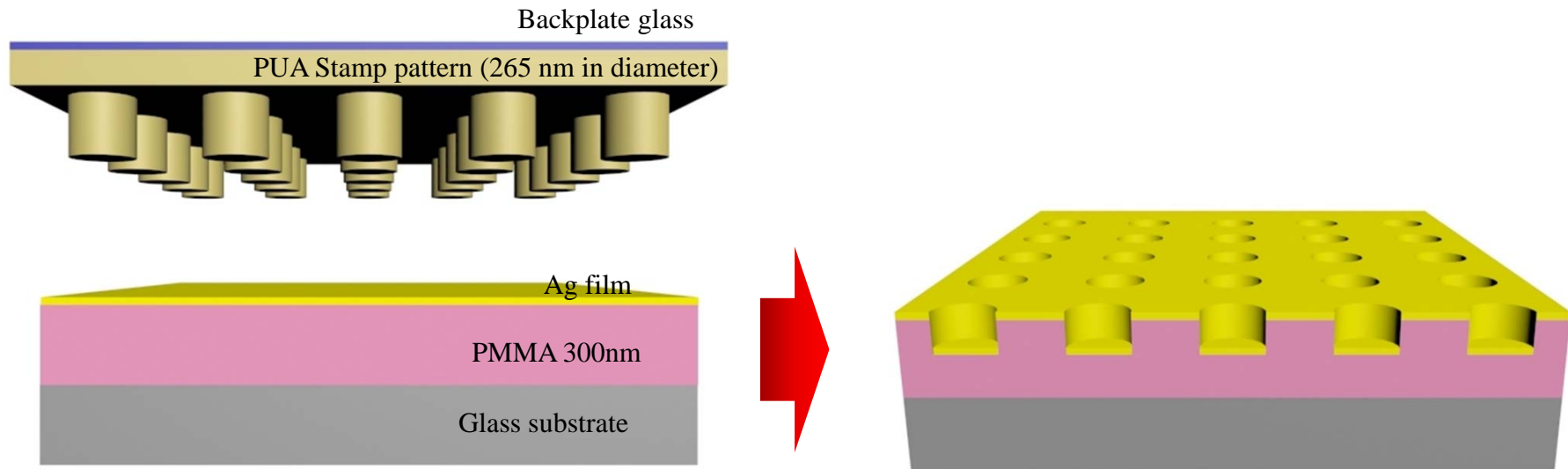
□ Front Illumination & Reflection



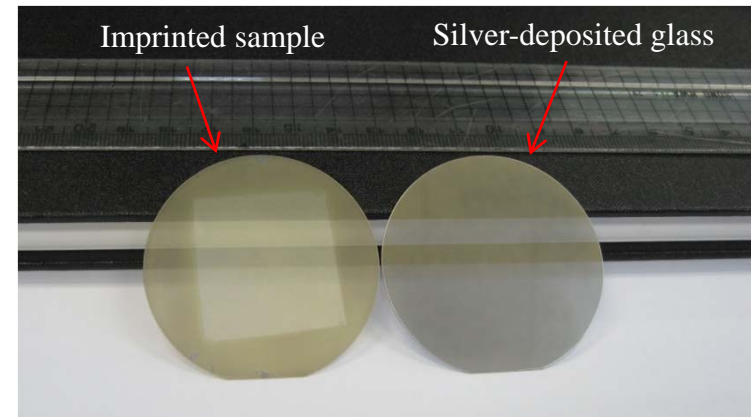
3D Under-cut shape Au nanopatterning

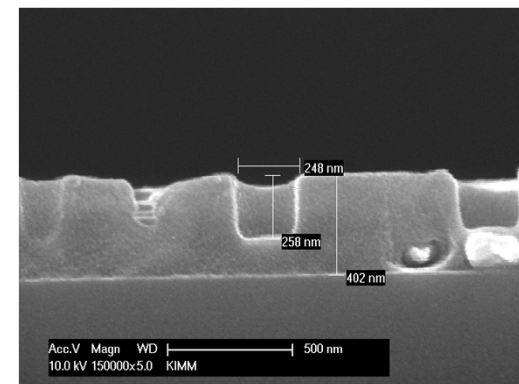
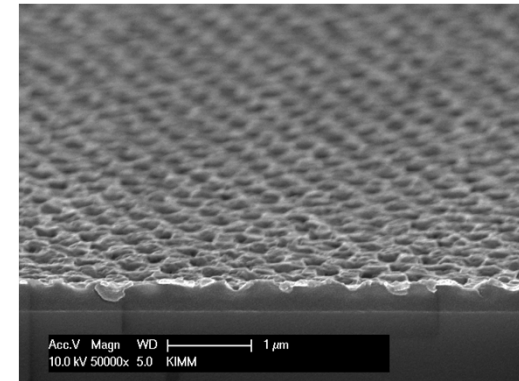
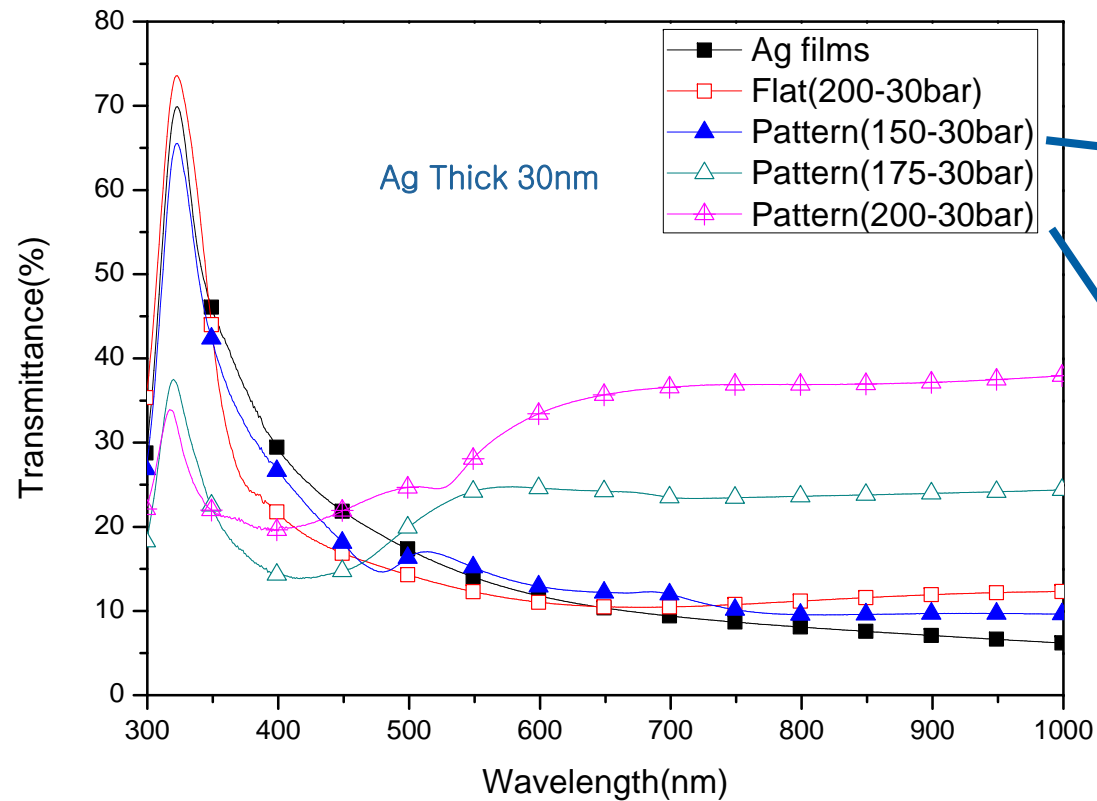


■ Metal direct nanoimprint (corrugation)



- Implement PMMA layer as a cushioning buffer
- Increased Transmittance
- Configurable for depth, hole shape
- Applicable to UV-resistant tint glass



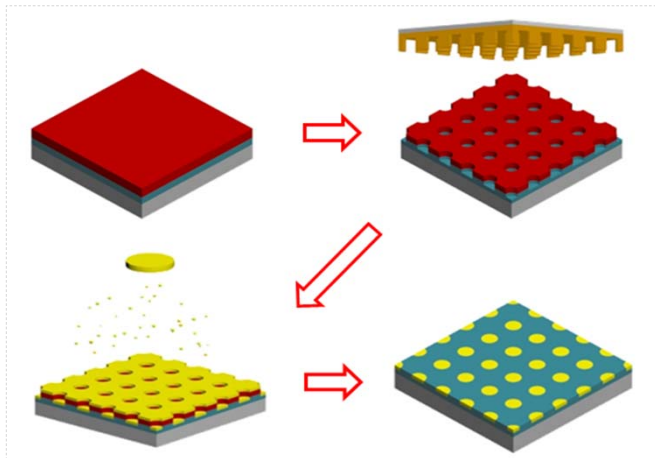


- Temperature dependent
 - Metal diffusion, followed by silver self-aggregation
- Pressure dependent, namely structure dependent
 - Optical modulation through Index-gradient

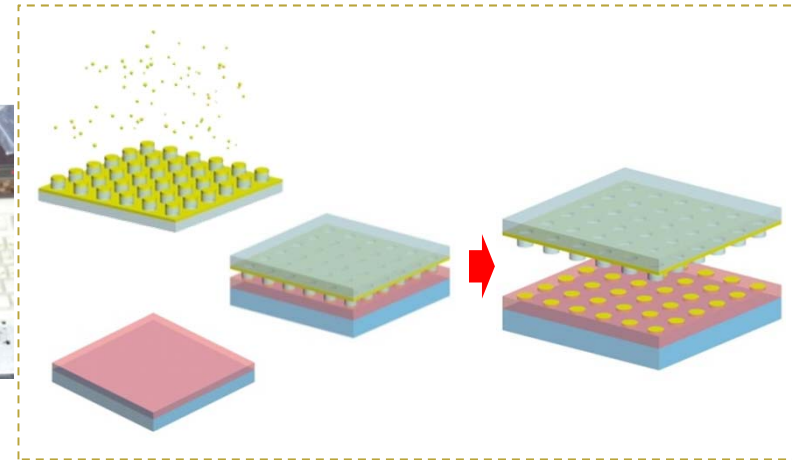
- Patent registered
- JJAP, 2013

Embedded Ag nanopatterning

1. Bilayer Hybrid NIL → Lift-off

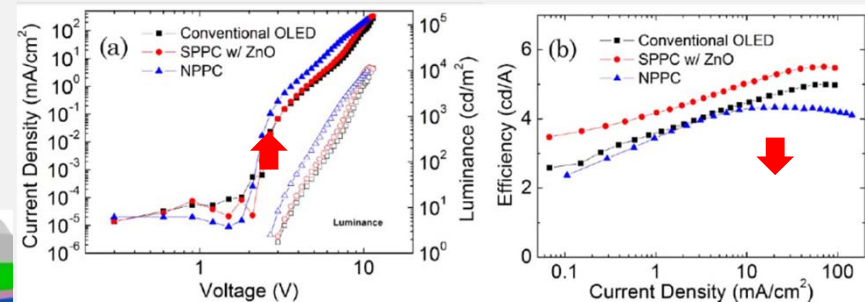
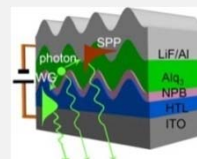


2. Metal Imprint Transfer



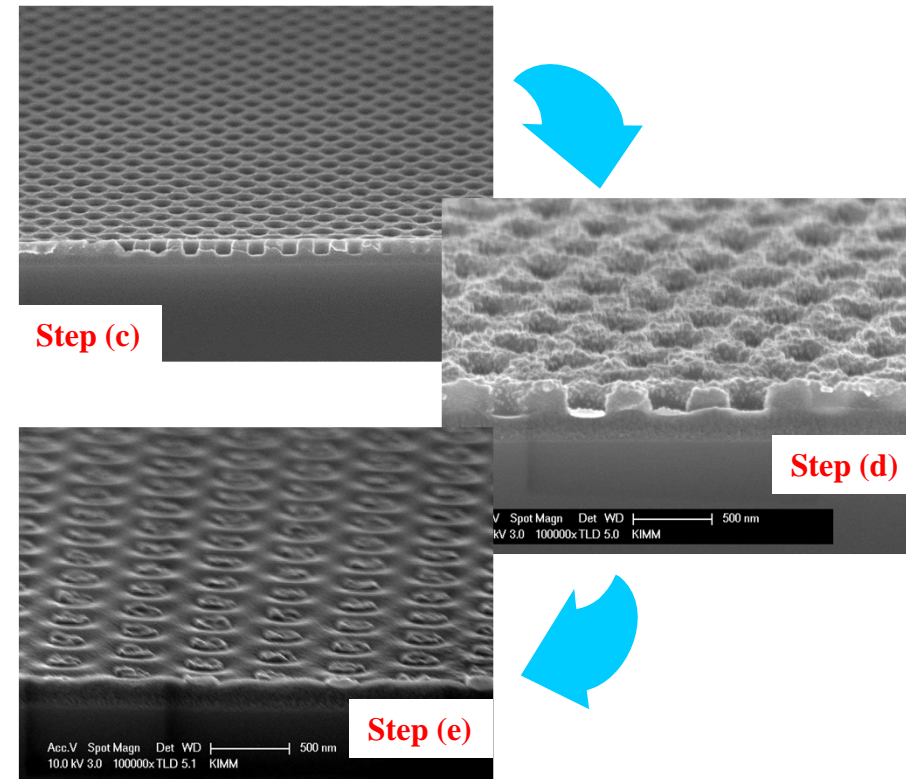
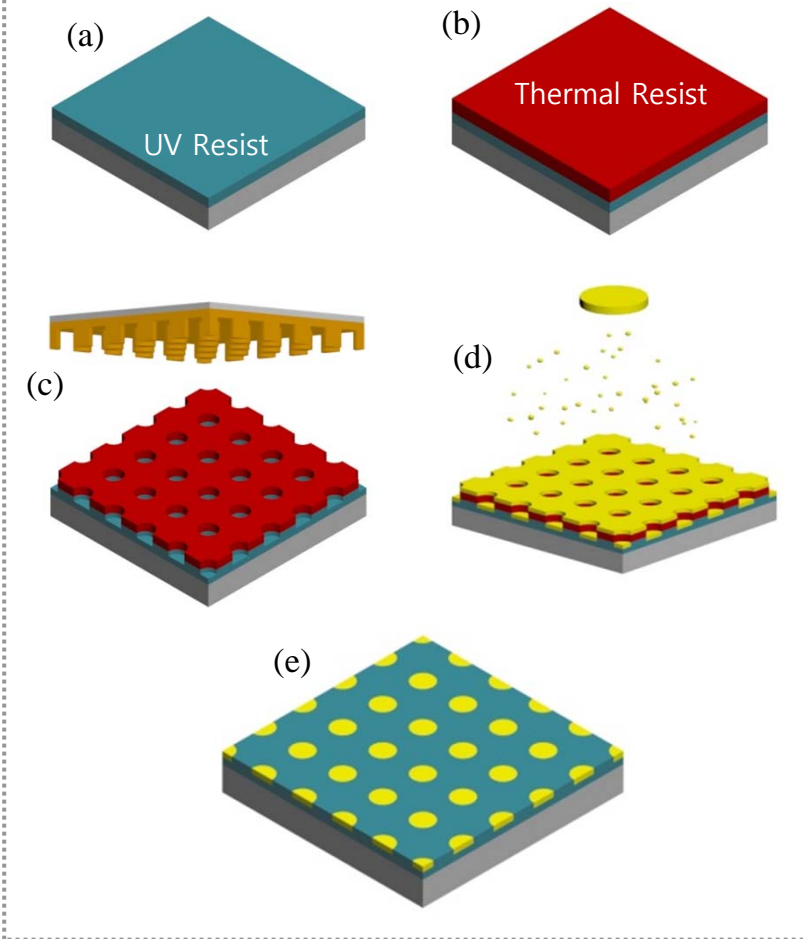
Point of the embedded configuration on multilayer optoelectronics

- Non-uniform current flow (Leakage current)
⇒ Reduced efficiency
- Electric field/Electron injection enhancement
⇒ Electron transport effects
⇒ Thermal decay, reduced life-time
- Charge recombination in solar cells
⇒ Thermal decay, reduced efficiency

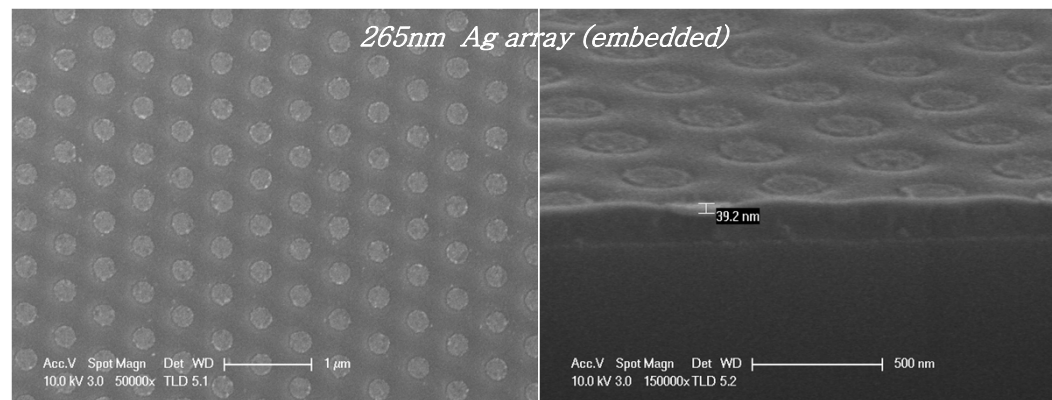
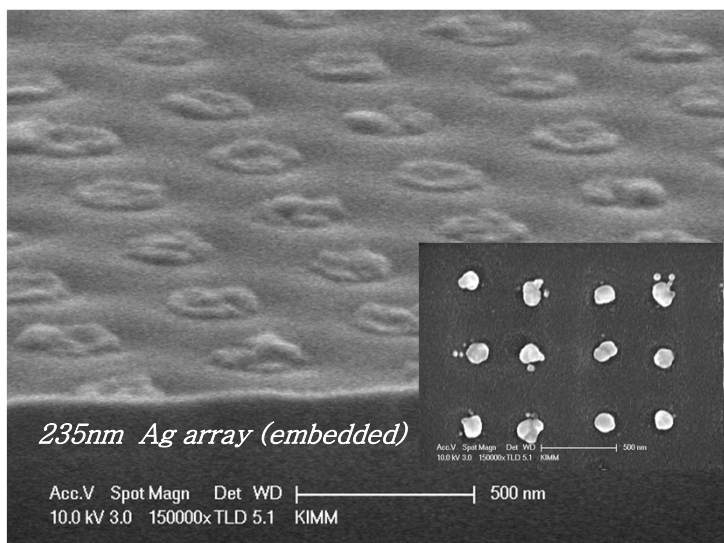
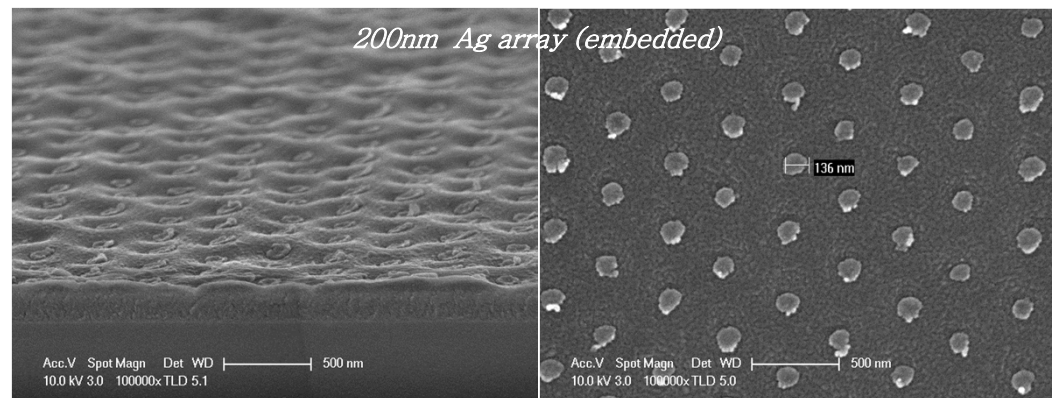
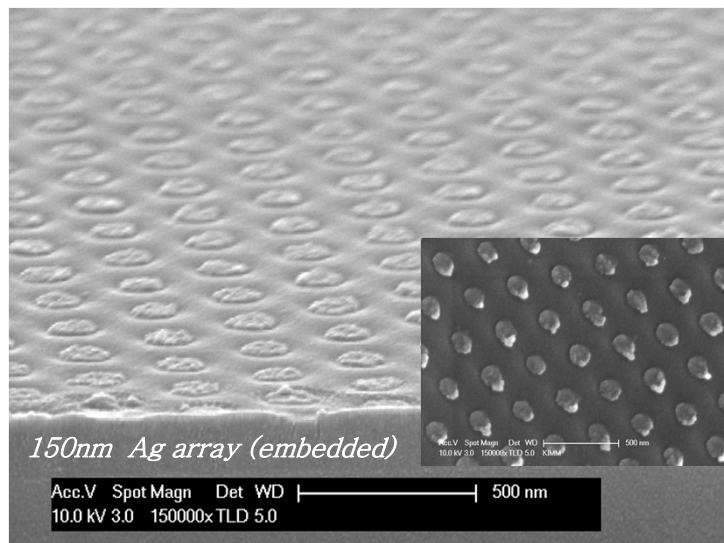


1. Bilayer Hybrid NIL and following Lift-off

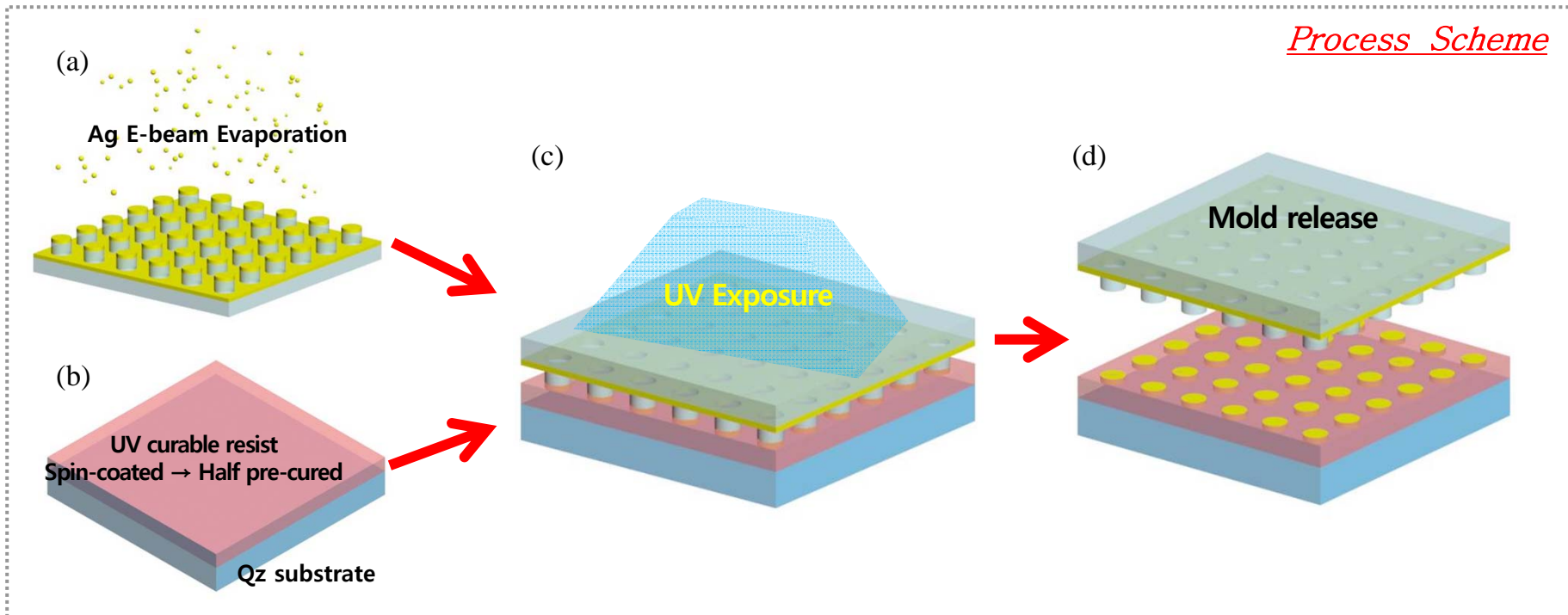
Process Scheme



- (a) Half pre-cure of the coated UV resist
- (c) Thermal Imprint, UV exposure while pressed
-> Complete cure of UV resist
- (d) Ag deposition by E-beam evaporation
- (e) Lift-off of thermal resist on top layer



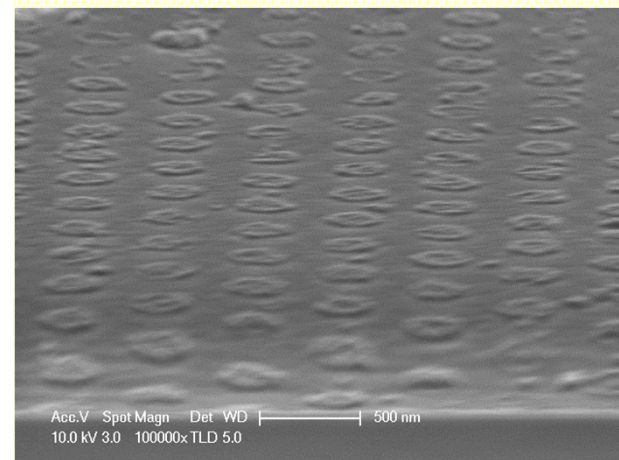
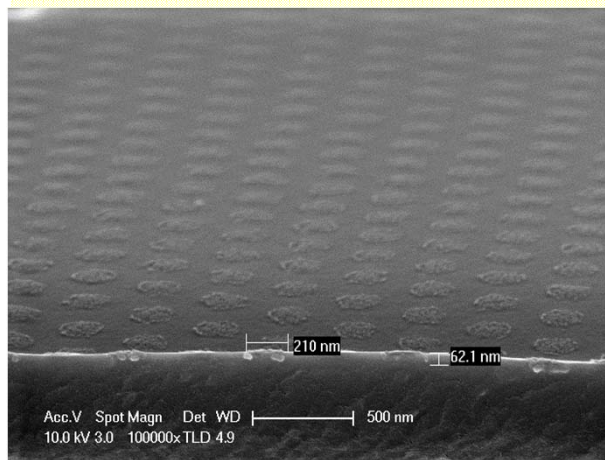
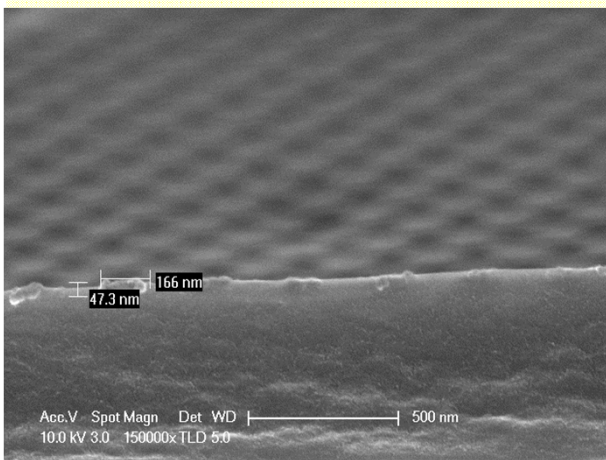
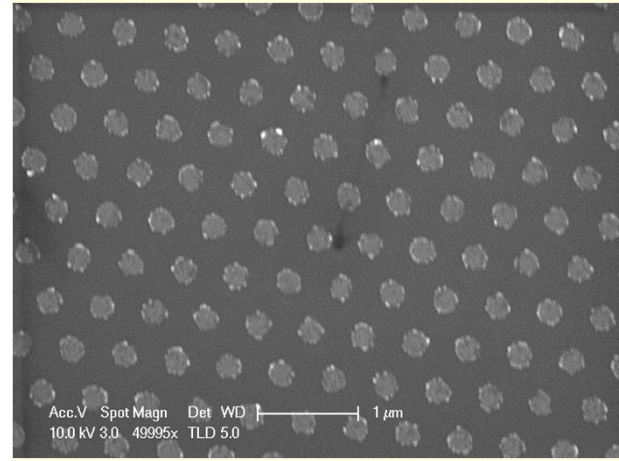
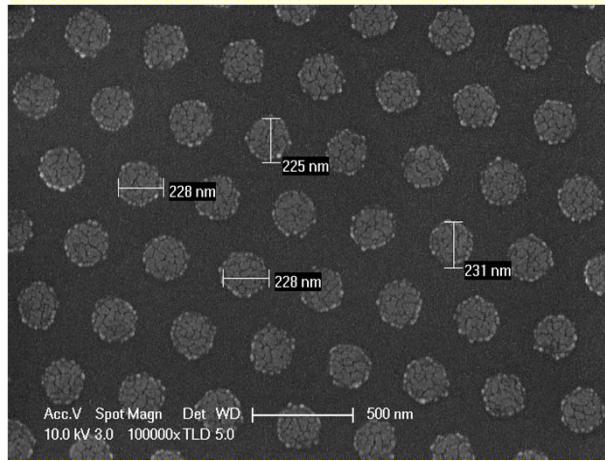
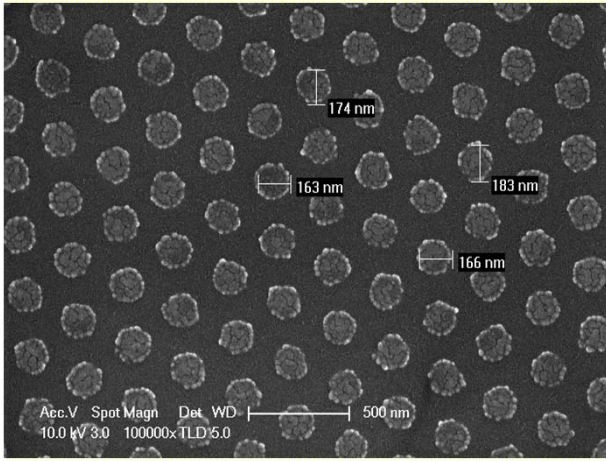
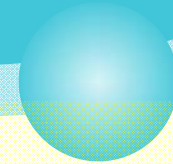
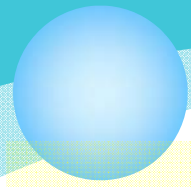
2. Metal Imprint Transfer



- (a) E-beam evaporation of Ag on mold pattern
- (b) Half pre-cure of UV curable resist
- (c) Only Ag on the top surface of mold in contact with half-cured resist, and UV exposure
- (d) Mold release

⌘ *Note: Embedded Dot Arrays*

- 1) Half pre-cured UV-NIL resist
- 2) Limitedly Imprinted Depth via Pressure Control
- 3) Minimized Leakage Current at device depo.



150nm Imprint-Transferred

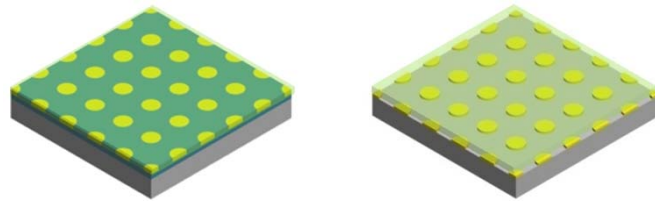
200nm Imprint-Transferred

265nm Imprint-Transferred

3. PL Enhancements on Green w.r.t. configuration

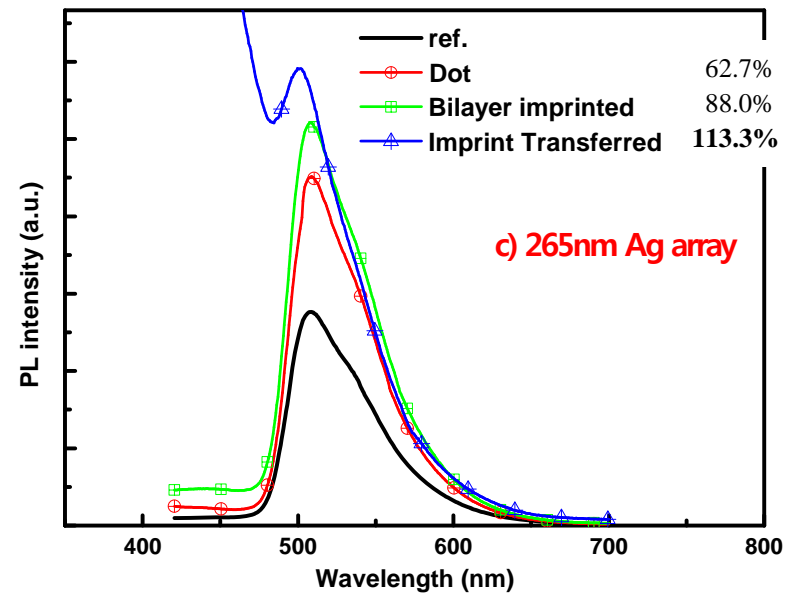
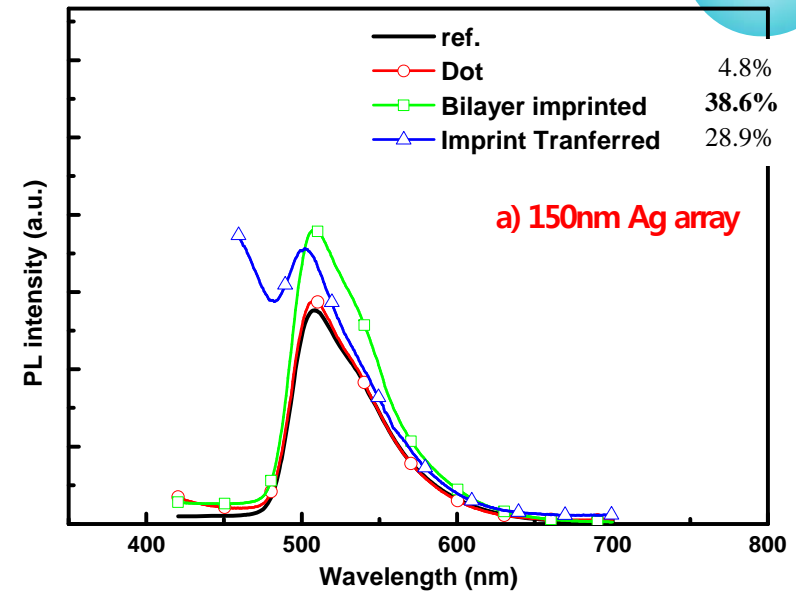
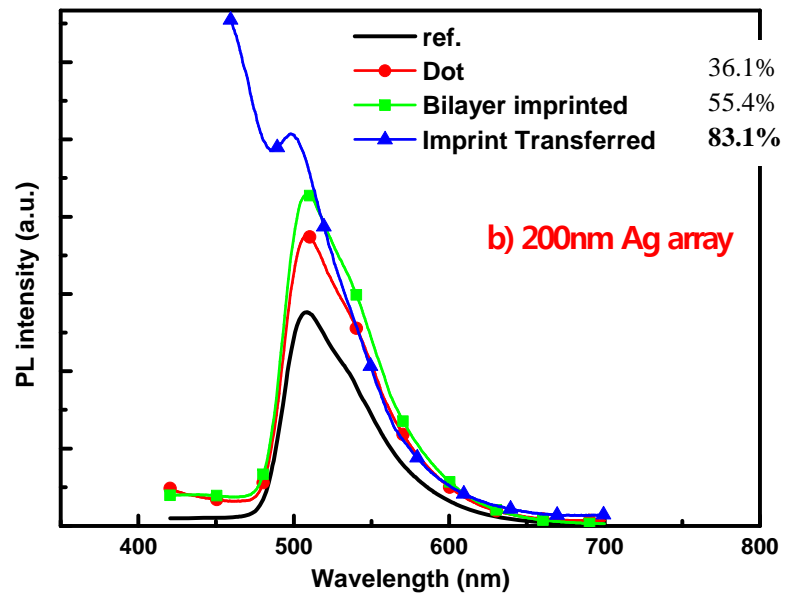
Comparison of PL enhancement for Green

* Ag pattern array / SiO₂ 20nm / PL 증착

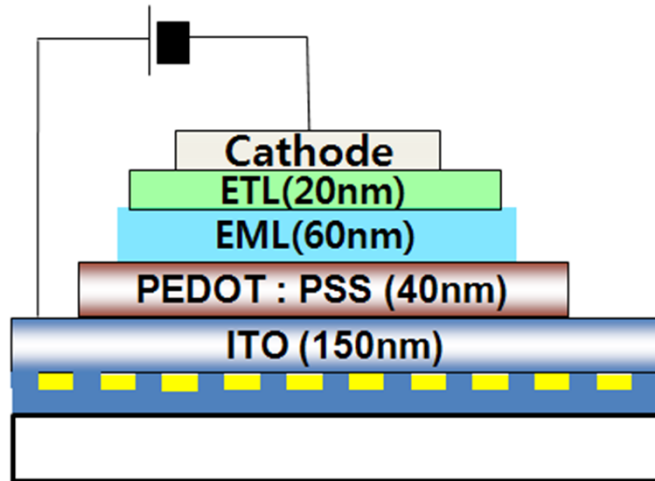


Embedded-config.

Dot-configuration

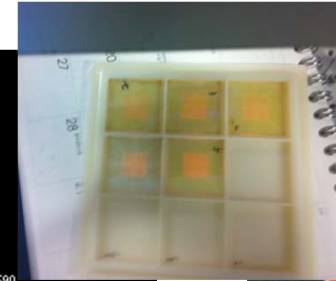
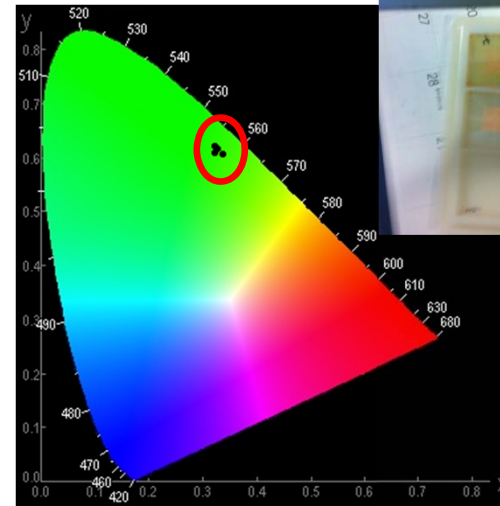


4. EL Enhancements on Green w.r.t. config.



⊙ Host – PVK/OXD-7, TCTA//TPBi

⊙ Dopant – Ir(mppy)₃ – green emitting material



Device configuration : ITO/PEDOT:PSS/EML/OXD-7/Al.

^b Turn-on voltage at 1 cd/m².

^c Maximum luminance.

^d Maximum luminance efficiency.

^e The 1993 CIE coordinates at maximum luminance

No.	Turn-On Voltage(V) ^b	L _{max} (cd/m ²) ^c	LE _{max} (cd/A) ^d	QE (%)	CIE Coordinates (X,Y) ^e
Ref.	8.98	1980	16.26	4.71	(0.338,0.604)
150 nm	5.99	2914	17.09	4.61	(0.322,0.618)
200 nm	5.79	2722	13.07	3.94	(0.328,0.614)
265 nm	7.14	2112	26.51	6.44	(0.323,0.607)

➔ EL enhanced by ~ 34% for 265nm

Closing Remarks

- Research Infrastructures for Nanostructures/Patterning
- Nanoimprint-based nanopatterning and its application works
- Several approaches for metallic nanopatterning fabrications introduced in plasmonic fields

- Global Collaborations

UC Berkeley–Micromechanical Analysis and Design (BMAD)

IMRE, Singapore

AMO GmbH Aachen



- Industrial Collaborations

APN

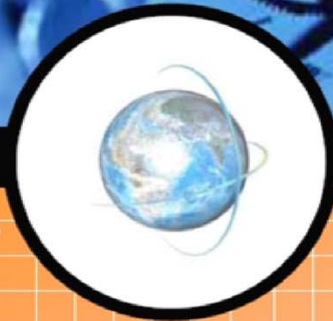
Hutem Co.

nanoLambda Korea Company

Samsung Electronics–Manufacturing Institute

Youngchang Chemical Co. Ltd





KIMM

Thank you for your attentions